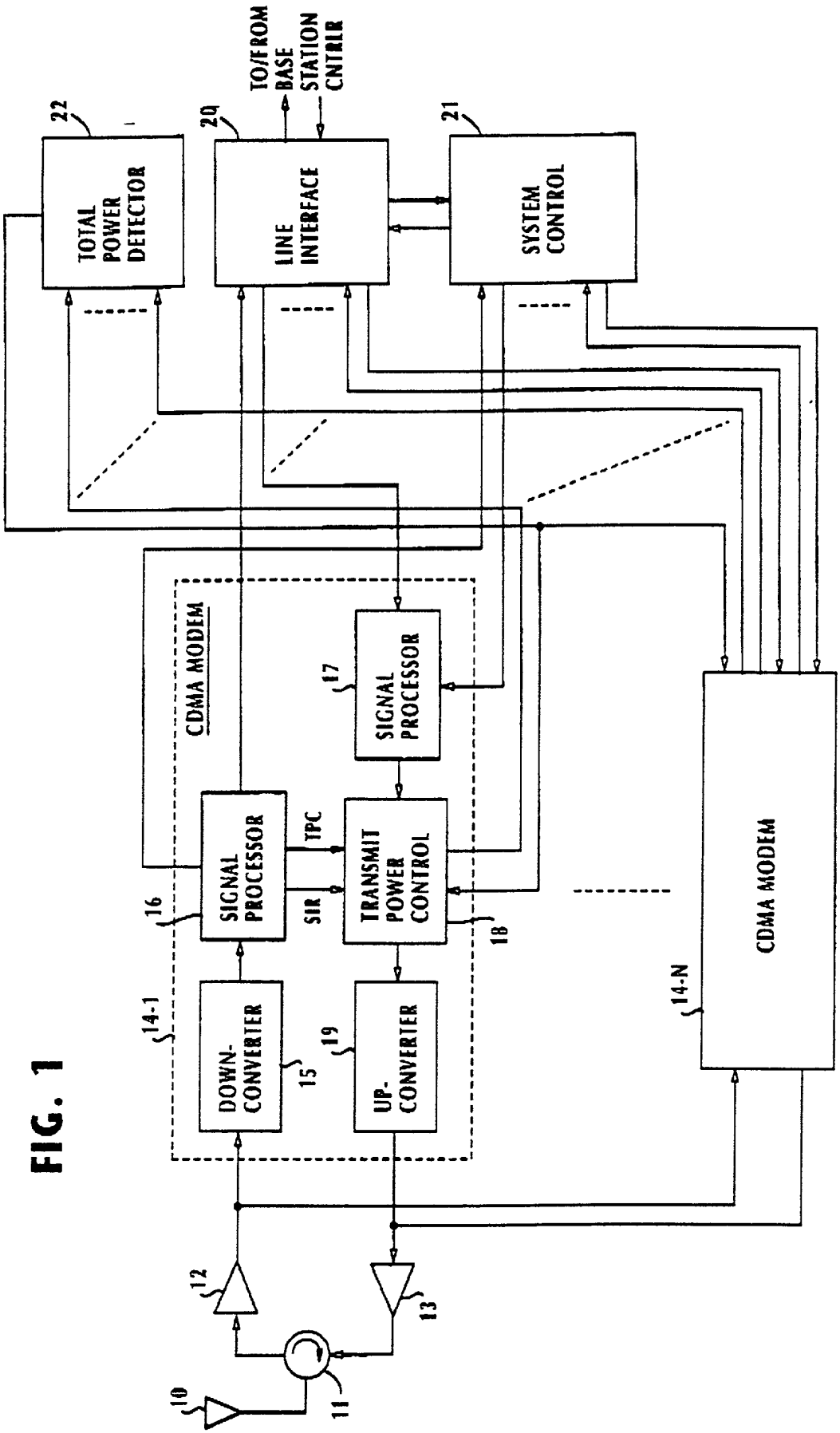

TO/FROM BASE STATION CNTRLR
22
20
21
TOTAL POWER DETECTOR
LINE INTERFACE
SYSTEM CONTROL
CDMA MODEM
17
SIGNAL PROCESSOR
TPC
SIR
SIGNAL PROCESSOR
TRANSMIT POWER CONTROL
16
18
14-1
DOWN-CONVERTER
15
19
UP-CONVERTER
14-N
CDMA MODEM
12
13
10
11

FIG. 1

FIG. 2

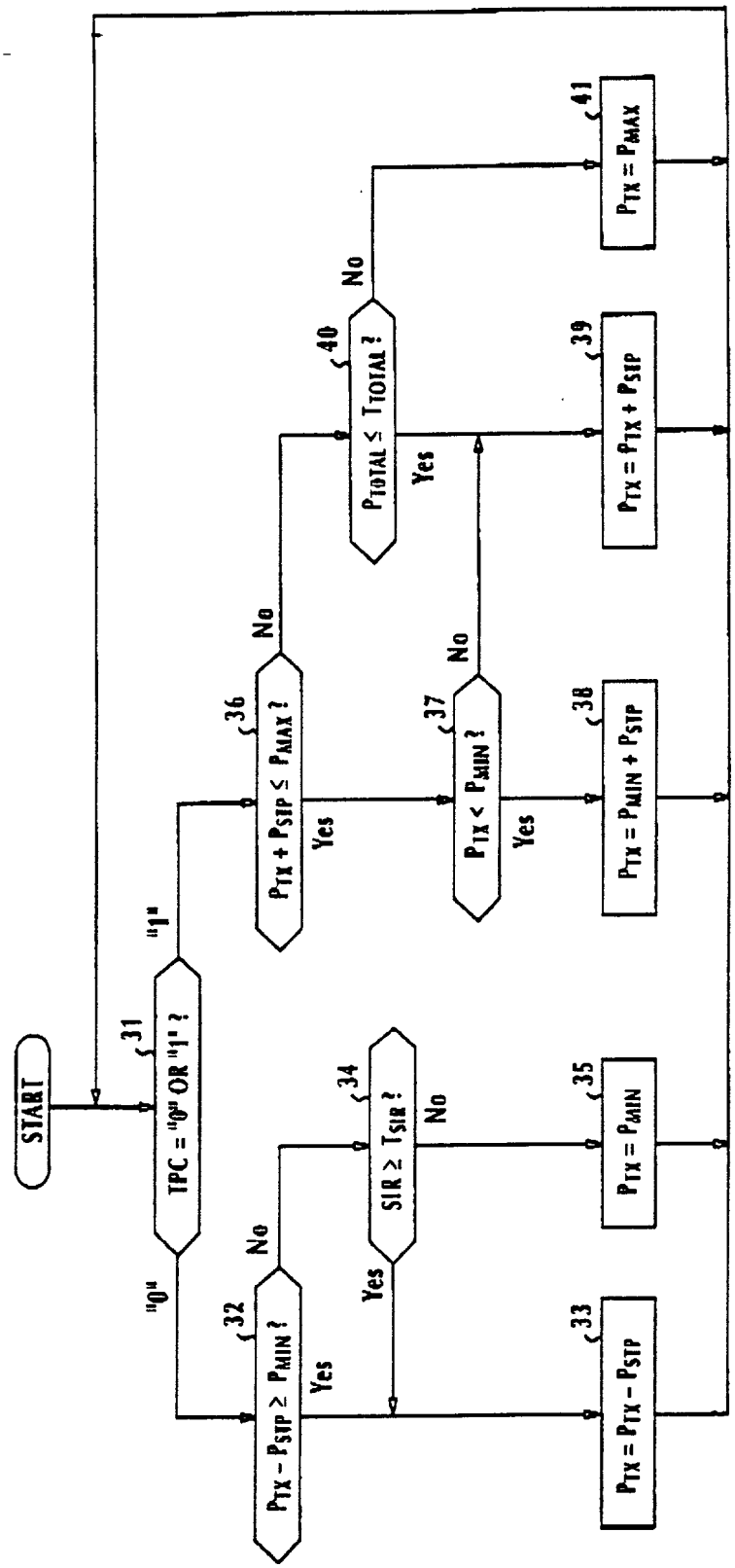

START
31
TPC = "0" OR "1" ?
"1"
"0"
36
PTX + PSTP ≤ PMAX ?
No
Yes
37
PTX < PMIN ?
No
Yes
38
PTX = PMIN + PSTP
40
PTOTAL ≤ TTOTAL ?
No
Yes
39
PTX = PTX + PSTP
41
PTX = PMAX
32
PTX - PSTP ≥ PMIN ?
No
Yes
34
SIR ≥ TSIR ?
No
Yes
35
PTX = PMIN
33
PTX = PTX - PSTP

FIG. 3

INTERRUPT ROUTINE

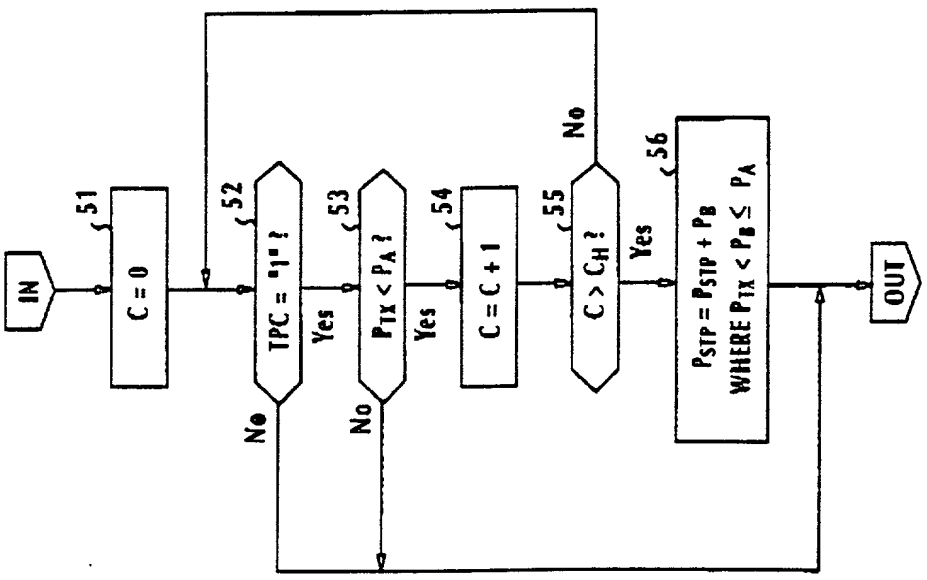
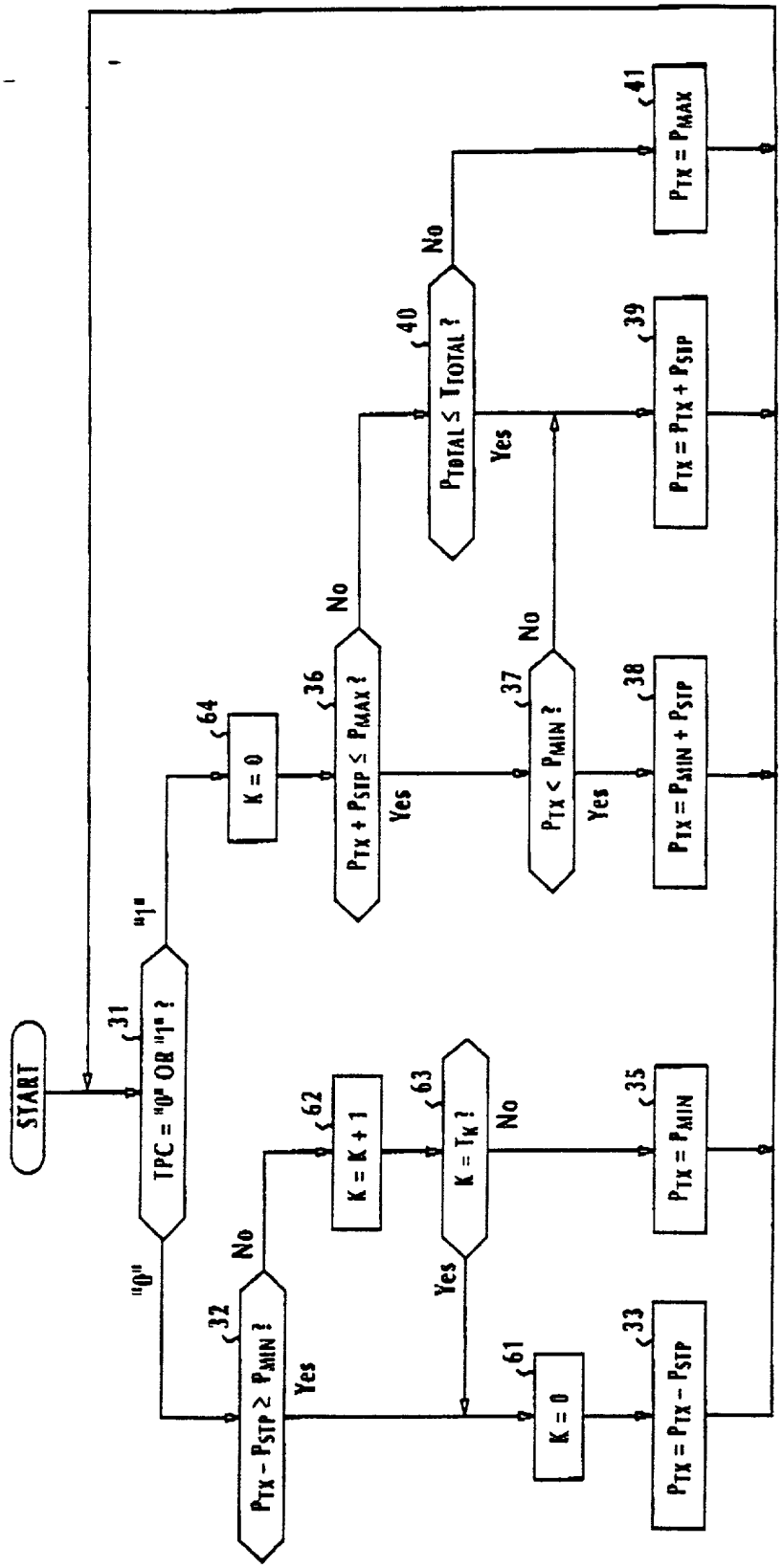


FIG. 4


START
31
TPC = "0" OR "1" ?
"1"
"0"
64
K = 0
36
PTX + PSTP ≤ PMAX ?
No
Yes
37
PTX < PMIN ?
No
Yes
40
PTOTAL ≤ TTOTAL ?
No
Yes
41
PTX = PMAX
39
PTX = PTX + PSTP
38
PTX = PMIN + PSTP
32
PTX - PSTP ≥ PMIN ?
No
Yes
62
K = K + 1
63
K = TK ?
No
Yes
35
PTX = PMIN
61
K = 0
33
PTX = PTX - PSTP